TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

GT30J101

High Power Switching Applications

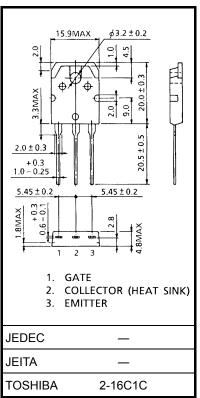
Unit: mm

- Third-generation IGBT
- Enhancement mode type
- High speed: $t_f = 0.30 \mu s \text{ (max)}$
- Low saturation voltage: $V_{CE (sat)} = 2.7 \text{ V (max)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	600	V	
Gate-emitter voltage		V _{GES}	±20	V	
Collector current	DC	IC	30	Α	
	1 ms	I _{CP}	60		
Collector power dissipation (Tc = 25°C)		P _C	155	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

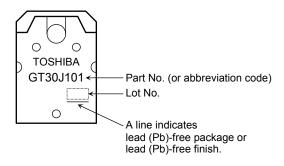


Weight: 4.6 g

operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

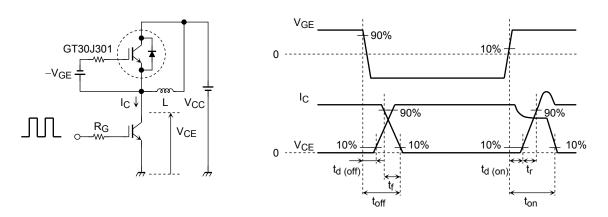
Marking



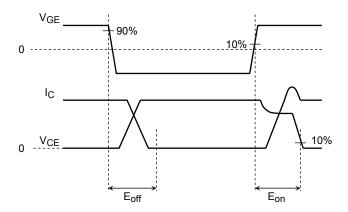
Electrical Characteristics (Ta = 25°C)

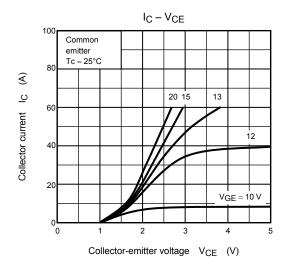
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage curre	ent	I _{GES}	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0$	_	_	±500	nA	
Collector cut-off cu	rrent	I _{CES}	V _{CE} = 600 V, V _{GE} = 0	_	_	1.0	mA	
Gate-emitter cut-of	f voltage	V _{GE} (OFF)	$I_C = 3$ mA, $V_{CE} = 5$ V	5.0	_	8.0	٧	
Collector-emitter saturation voltage		V _{CE} (sat)	$I_C = 30 \text{ A}, V_{GE} = 15 \text{ V}$	_	2.1	2.7	٧	
Input capacitance		C _{ies}	V _{CE} = 20 V, V _{GE} = 0, f = 1 MHz	_	2200	_	pF	
Switching time	Rise time	t _r	Inductive Load	_	0.12	_		
	Turn-on time	t _{on}	V _{CC} = 300 V, I _C = 30 A	_	0.40	_	μS	
	Fall time	t _f	$V_{GG} = \pm 15 \text{ V}, R_G = 43 \Omega$	_	0.15	0.30		
	Turn-off time	t _{off}	(Note1)	_	0.70	_		
Thermal resistance R		R _{th (j-c)}	_	_	_	0.81	°C/W	

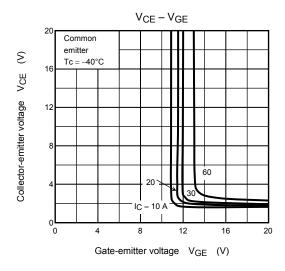
Note1: Switching time measurement circuit and input/output waveforms

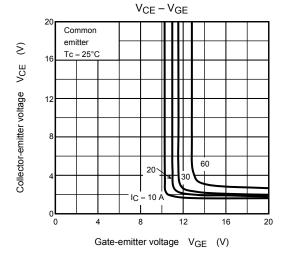


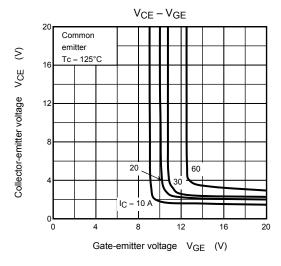
Note2: Switching loss measurement waveforms

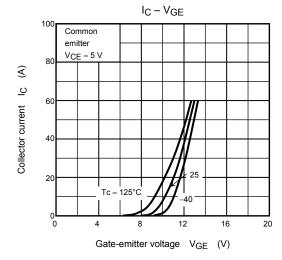


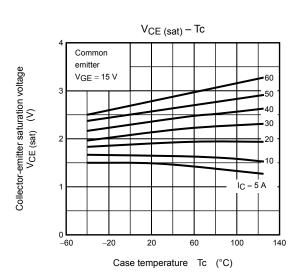




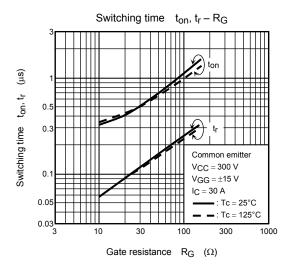


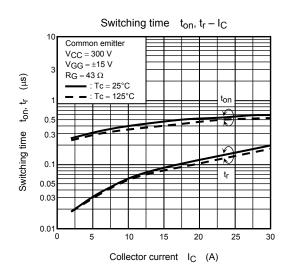


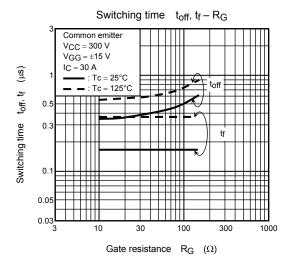


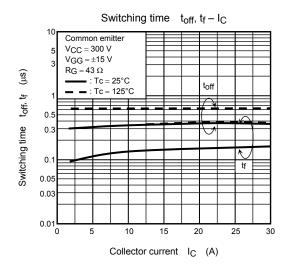


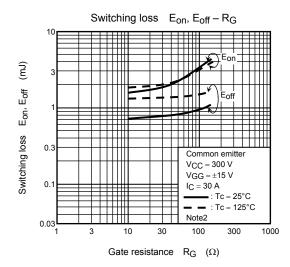
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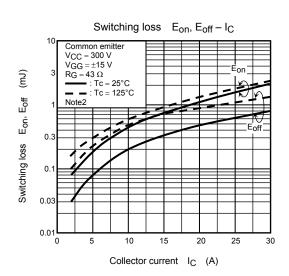


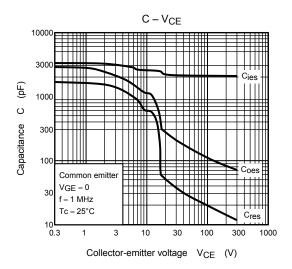


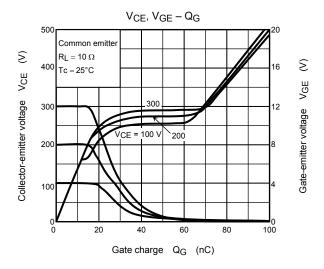


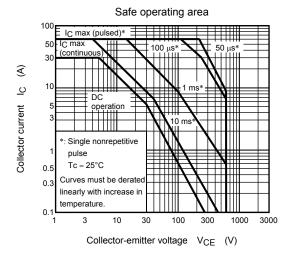


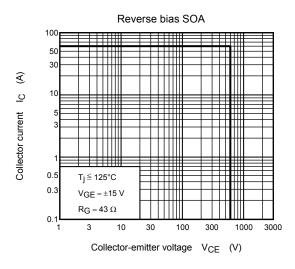


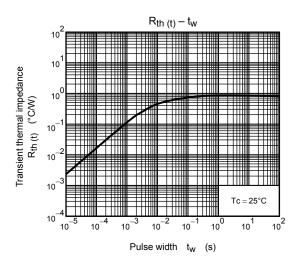












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